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L5	7	(phase adj detector) and (inner near2 decoder) and (phase adj error) and (outer near2 decoder) and (loop adj filter) and (Phase adj accumulator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 15:59
L6	7	(phase adj detector) and (inner near2 decoder) and (phase adj error) and (outer near2 decoder) and (loop adj filter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 15:58
L7	7	(phase adj detector) and (inner with decoder) and (phase adj error) and (outer with decoder) and (loop adj filter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 15:58

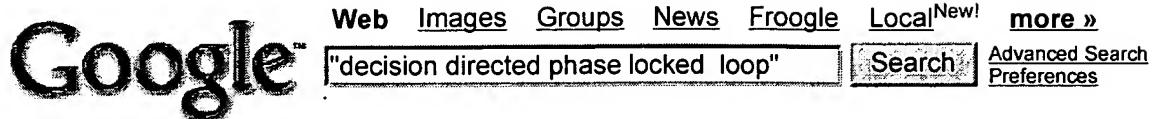
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6.1. Multichannel can be computed by a first-order **decision-directed phase-locked loop** [3]

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